

10660310_CLSTITLES1.txt

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10660310 on March 07, 2006

7 711/118 (4 OR, 3 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching

7 711/144 (3 OR, 4 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/141 ...Coherency
711/144Cache status data bit

6 711/146 (1 OR, 5 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/141 ...Coherency
711/146Snooping

6 711/3 (2 OR, 4 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/1 ADDRESSING COMBINED WITH SPECIFIC MEMORY
CONFIGURATION OR SYSTEM
711/3 .Addressing cache memories

5 711/145 (1 OR, 4 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/141 ...Coherency
711/145Access control bit

4 711/119 (0 OR, 4 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/119 ...Multiple caches

4 711/133 (1 OR, 3 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/133 ...Entry replacement strategy

3 710/22 (1 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA

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PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/22 .Direct Memory Accessing (DMA)

3 711/130 (2 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/130 ...Shared cache

3 711/135 (2 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/133 ...Entry replacement strategy
711/135Cache flushing

3 711/138 (1 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/138 ...Cache bypassing

3 711/143 (1 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/141 ...Coherency
711/143write-back

3 711/147 (1 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/147 .Shared memory area

3 711/207 (1 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/200 ADDRESS FORMATION
711/202 .Address mapping (e.g., conversion,
translation)
711/203 ..Virtual addressing
711/206 ...Translation tables (e.g., segment and page
table or map)
711/207Directory tables (e.g., DLAT, TLB)

3 712/23 (0 OR, 3 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/1 PROCESSING ARCHITECTURE
712/23 .Superscalar

2 709/213 (0 OR, 2 XR)

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Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MULTIPLE COMPUTER OR PROCESS COORDINATING
709/213 MULTICOMPUTER DATA TRANSFERRING VIA SHARED
MEMORY

2 710/105 (0 OR, 2 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS
TRANSACTION PROCESSING)
710/105 .Protocol

2 710/108 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS
TRANSACTION PROCESSING)
710/107 .Bus access regulation
710/108 ..Bus locking

2 710/52 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/52 .Input/Output data buffering

2 711/100 (1 OR, 1 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL

2 711/113 (1 OR, 1 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/101 .Specific memory composition
711/111 ..Accessing dynamic storage device
711/112 ...Direct access storage device (DASD)
711/113Caching

2 711/121 (1 OR, 1 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/119 ...Multiple caches
711/121Private caches

2 711/122 (1 OR, 1 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/119 ...Multiple caches
711/122Hierarchical caches

2 711/125 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories

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711/118 ..Caching
711/125 ...Instruction data cache

2 711/139 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/138 ...Cache bypassing
711/139 No-cache flags

2 711/140 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/140 ...Cache pipelining

2 711/156 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/154 .Control technique
711/156 ..Status storage

2 711/159 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/154 .Control technique
711/159 ..Entry replacement strategy

2 711/165 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/154 .Control technique
711/165 ..Internal relocation

2 711/168 (2 OR, 0 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing
711/168 ..Concurrent accessing

2 711/169 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing
711/169 ..Memory access pipelining

2 711/200 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
711/200 ADDRESS FORMATION

2 711/203 (1 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY

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711/200 ADDRESS FORMATION
711/202 .Address mapping (e.g., conversion,
 translation)
711/203 ..virtual addressing

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Most Frequently Occurring Classifications of Patents Returned
From A Search of 10660310 on March 07, 2006

Original Classifications

4 711/118
3 711/144
2 711/130
2 711/135
2 711/168
2 711/3

Cross-Reference Classifications

5 711/146
4 711/119
4 711/144
4 711/145
4 711/3
3 711/118
3 711/133
3 712/23
2 709/213
2 710/105
2 710/22
2 711/125
2 711/138
2 711/139
2 711/140
2 711/143
2 711/147
2 711/156
2 711/159
2 711/165
2 711/169
2 711/200
2 711/207

Combined Classifications

7 711/118
7 711/144
6 711/146
6 711/3
5 711/145
4 711/119
4 711/133
3 710/22
3 711/130
3 711/135
3 711/138
3 711/143
3 711/147
3 711/207
3 712/23
2 709/213
2 710/105
2 710/108
2 710/52
2 711/100
2 711/113
2 711/121
2 711/122
2 711/125
2 711/139

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2 711/140
2 711/156
2 711/159
2 711/165
2 711/168
2 711/169
2 711/200
2 711/203

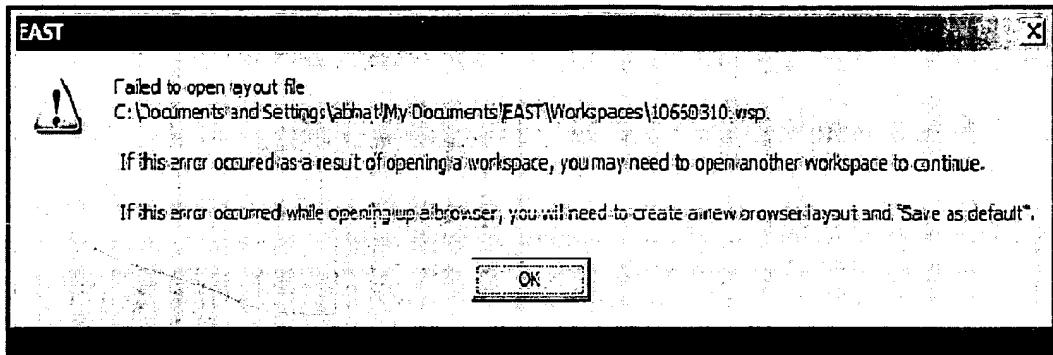
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5230045
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PLUS Search Results for S/N 10660310, Searched March 07, 2006

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

6088799
6970985
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10660310_WDS1.txt

abstract 1
accordance 1
adapt 1
adaptive 3
algorithm 3
an 1
and 4
any 2
applicable 1
application 1
architecture 2
as 3
assistants 1
at 4
aware 1
back 1
background 1
based 6
battery 1
be 14
begins 1
between 1
bij 1
brian 1
brief 1
by 1
cache 9
caching 1
cameras 1
case 1
cases 1
cease 1
cellular 1
chart 1
complicate 1
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computers 1
conditions 1
control 3
conventional 1
correctly 2
coulson 1
coupled 3
create 1
critical 1
data 2
date 1
depiction 1
description 2
detailed 1
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digital 2
disclosure 1
disk 2
disturbs 1
down 1
drawings 1
drive 1
driver 1
electric 1
electrodes 1
electronic 2

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embodiment 4
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environments 2
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example 2
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fax 1
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ferroelectric 5
figure 4
flash 1
flow 1
for 11
freewamuste 1
from 1
function 2
generally 1
he 1
higher 5
houston 1
however 1
hu 1
hub 4
if 1
in 9
include 2
including 1
input 2
invention 5
inventors 1
is 5
itl 2
its 1
katg 1
laptop 1
leete 1
letters 1
loss 1
ma 1
many 1
may 19
memories 6
memory 18
mobile 3
need 1
no 1
not 1
notebook 1
of 16
on 3
one 5
operate 1
operating 1
operation 2
or 3
order 1
other 1
output 2
page 4
pair 1

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patent 1
personal 2
polymer 7
power 1
prepared 1
present 4
problems 1
processor 6
pruner 1
purposes 1
range 1
rate 1
ready 1
reduce 1
reduction 1
referring 1
relates 1
relies 1
rely 1
remainder 1
rest 1
richard 1
schematic 1
sensitive 9
september 1
shutdown 2
since 1
slow 1
slower 1
some 1
speed 2
state 1
states 1
subject 1
subjected 1
such 2
system 7
systems 5
telephones 1
temperature 12
temperatures 5
tendency 1
that 3
the 24
there 1
these 1
this 2
threaten 1
through 1
thus 2
timothm 1
title 1
to 16
transfer 1
transitioned 1
trop 2
turn 1
tx 1
types 1
unexpected 3
united 1
us 3
uses 1

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which 2
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with 1
within 1
without 1
write 2